

IN THE SPECIFICATION:

Please amend paragraph [0042] as follows:

[0042] For example, on an AND gate with three inputs, terminal-names could be AND/P, AND/P and AND/P, essentially making them equivalent. This ensures that in sorting, these connections can be re-sorted according to child connections. Note that certain arithmetic functions, specifically commutative functions, have permutable input terminals, however it is slightly more complex. For example, $A+B$ is equivalent to $B+A$, but if one bit is permuted, all bits must be permuted. When sorting nodes in a particular rank it may happen that there are two or more ~~nodes~~ nodes having the same “master-name” and the same “terminal name”. In this case, there is no certain way to determine the order of the nodes. In such a case, all the possible permutations are generated and added to the child nodes list.